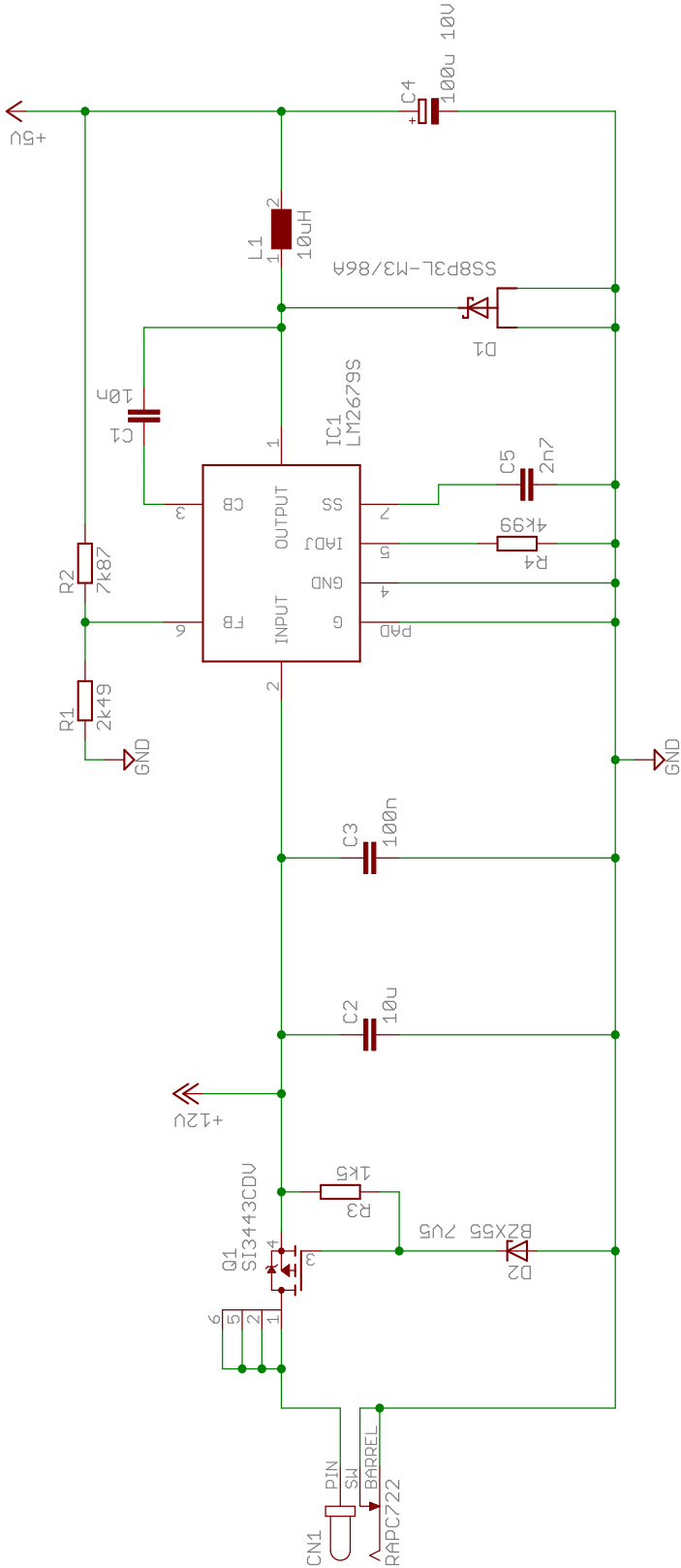


POWER SUPPLY SECTION (1)



NOTES

1. R1 and R2 are feedback resistors for the LM2679. If IC2 = LM2679S-5.0, R1=open and R2=0R. If IC2 = LM2679S-ADJ, R1=2.49k (2.5k), R2=7.87k (7.83k).
2. C5 (Softstart Cap) optional. Open-circuit = no soft-start.
3. Q3 provides reverse polarity protection for IC2, etc. IT IS NOT CONNECTED BACKWARDS! R3/D2 limit Vgs to 7.5V

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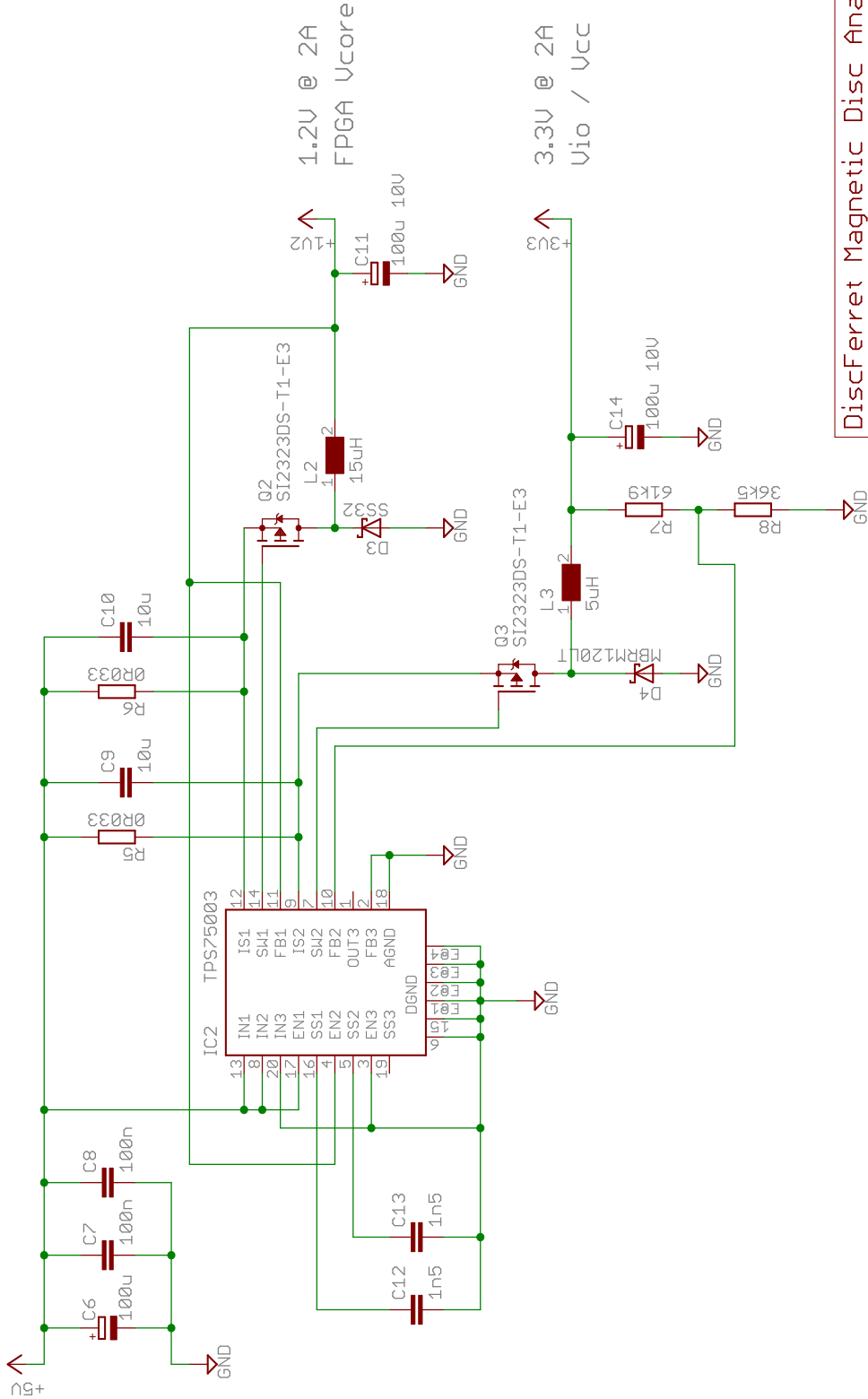
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POWER SUPPLY SECTION (2)



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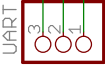
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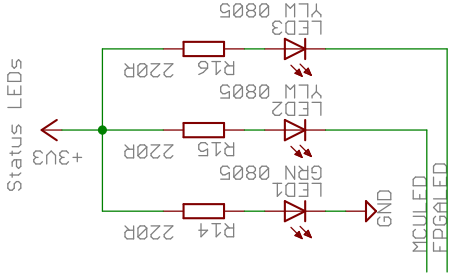


JP2 = UART

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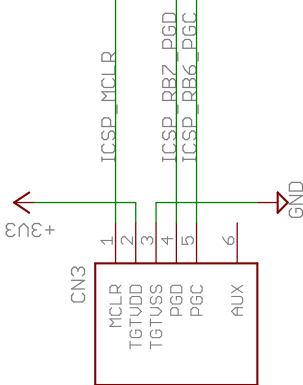
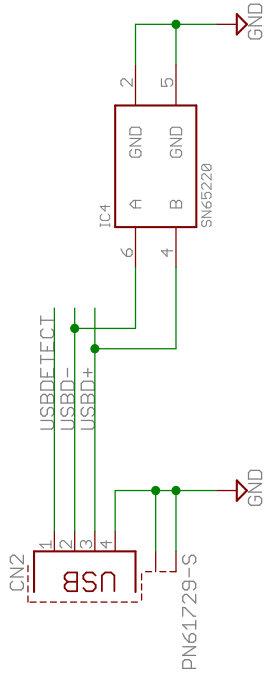
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LEDs, USB INTERFACE AND ICSP



LED1 = 3V3 status (power on)
LED2 = MCU status
LED3 = FPGA status

IC4 = Transient suppressor
May be omitted, but WILL IMPAIR USB port ESD performance!



In-Circuit Serial Programming

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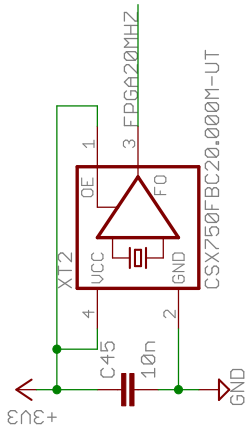
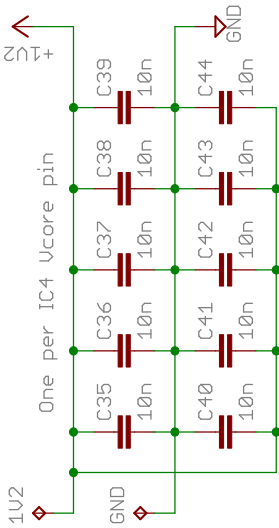
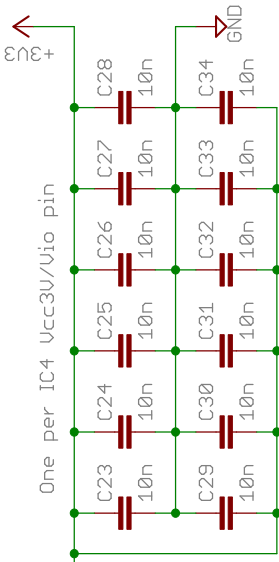
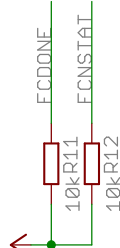
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FPGA



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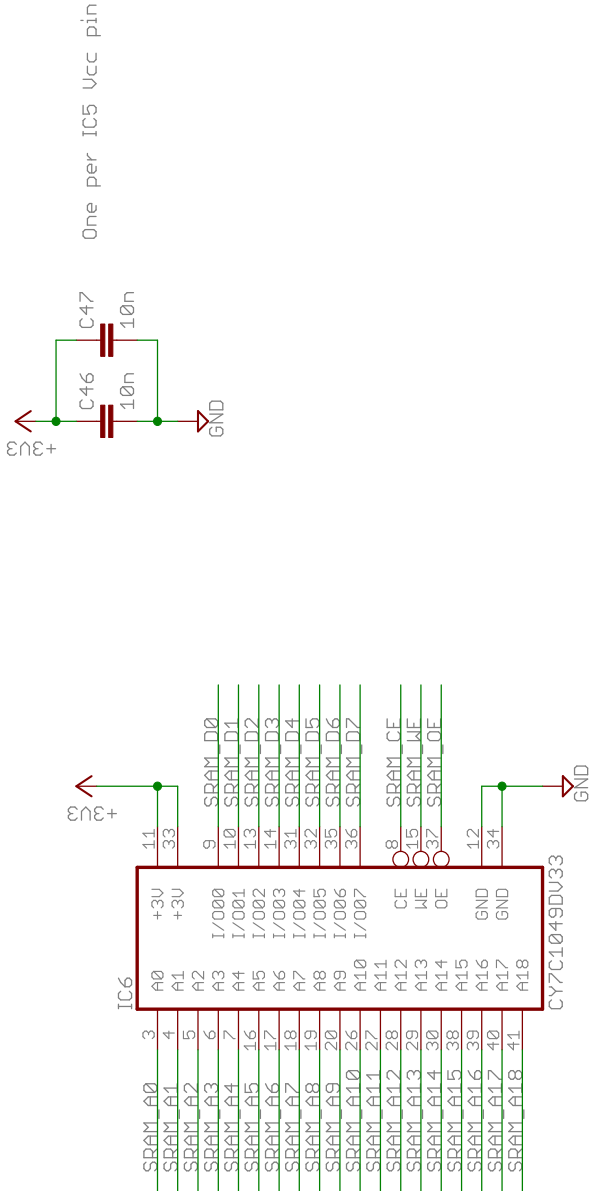
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Alternates for IC5:
GS74108AGP-10I
AS7C34096A-12TIN

(GSI) 10ns
(Alliance) 12ns

ACQUISITION MEMORY



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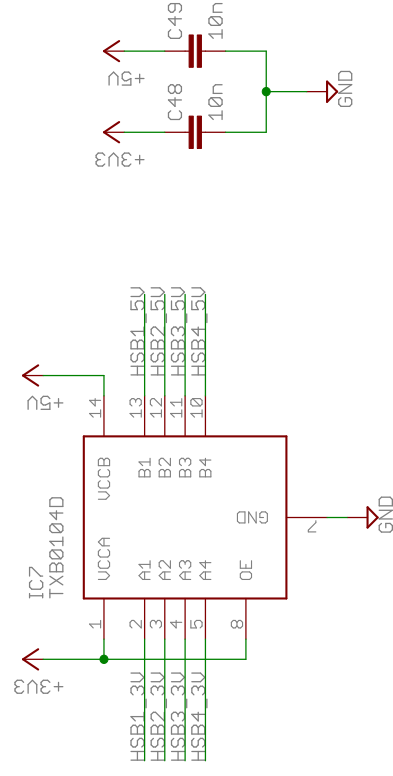
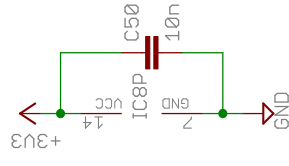
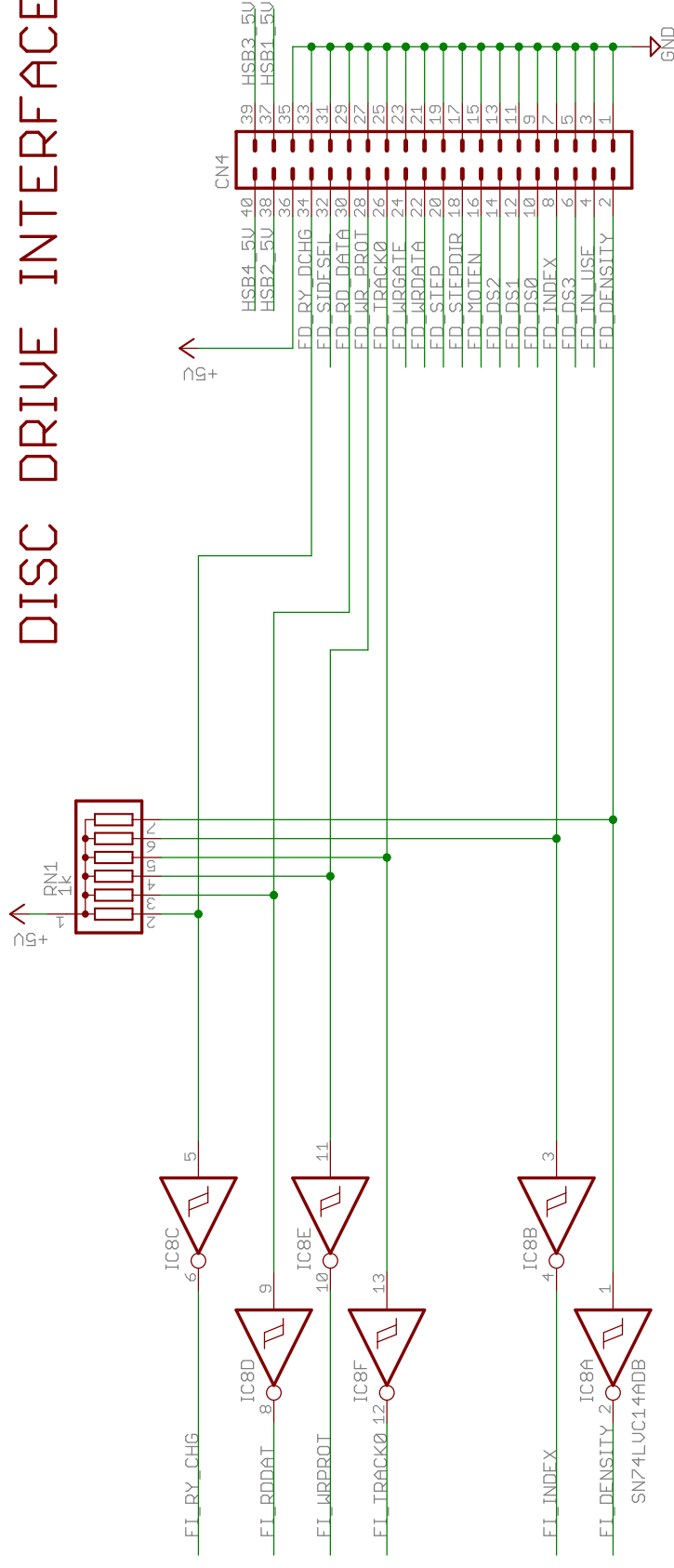
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DISC DRIVE INTERFACE (1)



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